# Carlos Eduardo Gallo Filho

Curriculum Vitae

### Education

**Bachelor's in Computer Science**, *FFCLRP – University of São Paulo*, Ribeirão Preto **2022–Present** – SP – Brazil.

Willing to finish in the end of 2024 (there is only one remaining course).

**Bachelor's in Medical Physics**, *FFCLRP – University of São Paulo*, Ribeirão Preto – **2018–2021** SP – Brazil.

Incomplete.

## Experience

Cloud Analyst, Opus Software, Remote.

03/2024-Present

- o General management tasks on a cloud infrastructure in AWS.
- O Usage of Docker, Kubernetes and Terraform technologies.
- O Management of monitoring and alerting tools, including Grafana and Prometheus.

**Developer**, *X.Org Foundation*, Remote, X.Org Endless Vacation of Code (EVoC) **06/2023–09/2023** Mentorship Program.

 I was mentored in a 3 month project working on increasing code coverage of the Linux kernel graphical subsystem (DRM) code by developing unit tests.

Infrastructure Analyst, Zeus A.I. DeepL, Ribeirão Preto – SP – Brazil.

09/2021-06/2023

 Management of GNU/Linux servers with LXD containers, Nginx web servers and reverse proxies, PostgreSQL databases.

#### Skills

**Programming** 

**Shellscript**: Advanced **C**: Intermediate

C++: Basic Python: Intermediate

Java: Basic SQL: Basic

Zig: Basic

**Technologies** 

GNU/Linux: Intermediate Git: Intermediate

## Academic Productions

**Undergraduate scientific research**, *Physics Department – FFCLRP – University of São* **2020–2021** *Paulo*, Ribeirão Preto – SP – Brazil.

#### Organic Field Effect Transistor with Electrolytic Gate Detection System Improvement

- o I designed a complete PCB from scratch controlled by a Raspberry Pi, which included: multiplexers, ADC converter, adjustable power source and instrumentation amplifiers.
- O I developed a Python program for measure control and data analysis, including real time graph plot.

**Undergraduate scientific research**, *Physics Department – FFCLRP – University of São* **2019–2020** *Paulo*, Ribeirão Preto – SP – Brazil.

#### Field Effect Transistor with Extended Gate Detection System Miniaturization

- I miniaturized a biosensor data acquisition system by designing an electronic circuit empowered by a Raspberry Pi.
- O I developed a Python program for measure control and data analysis.

8 bit computer: I'm currently making a breadboard 8 bit computer using 74xx IC family.

Game packet sniffer: I made TCP proxy in Zig that parses network packets of a specific online game.

 $\textbf{Snake Game}: \ A \ 2.5D \ snake \ game \ made \ using \ C++, \ FreeGLUT \ and \ plain \ OpenGL.$ 

 $\begin{tabular}{ll} \textbf{Multispectral Image Acquisition}: An embedded system to take multi-spectral images using C++, ESP32 microcontroller and a handmade PCB. \end{tabular}$ 

Free Software Contributions

Linux Kernel: Mostly unit tests for DRM (graphics) subsystem.

Language | Portuguese: Native English: Intermediate